Hand in Glove – Complete Bounded Model Checking and Testing of Interlocking Systems

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Motivation

- Testing interlocking systems is well known to require test case selection from an unmanageable multitude of possibilities
- How can we perform a well-justified test suite with acceptable effort . . .
- . . . while increasing the confidence into the strength of the resulting test suite ?

Overview

- Model-based testing
- Validated test models
- Complete test suites
- How many test cases do we need naive approach
- A refined test strategy compositional reasoning plus equivalence class testing plus randomisation plus boundary value selection
- Conclusion

Model-based Testing

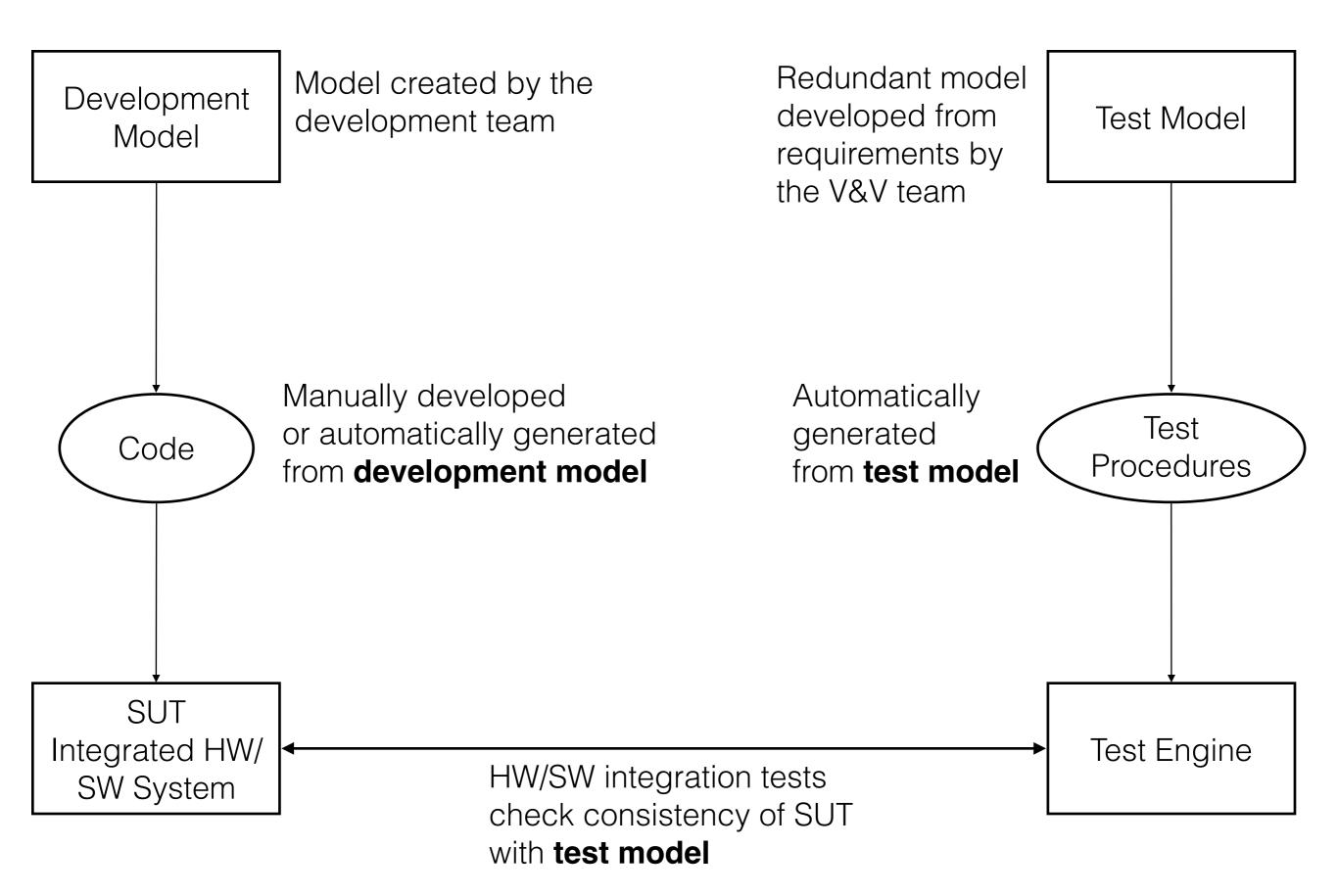
Instead of writing test procedures,

- develop a test model specifying expected behaviour of SUT
- use generator to identify "relevant" test cases from the model and calculate concrete test data
- generate **test procedures** fully automatic
- perform tracing requirements ↔ test cases in a fully automatic way

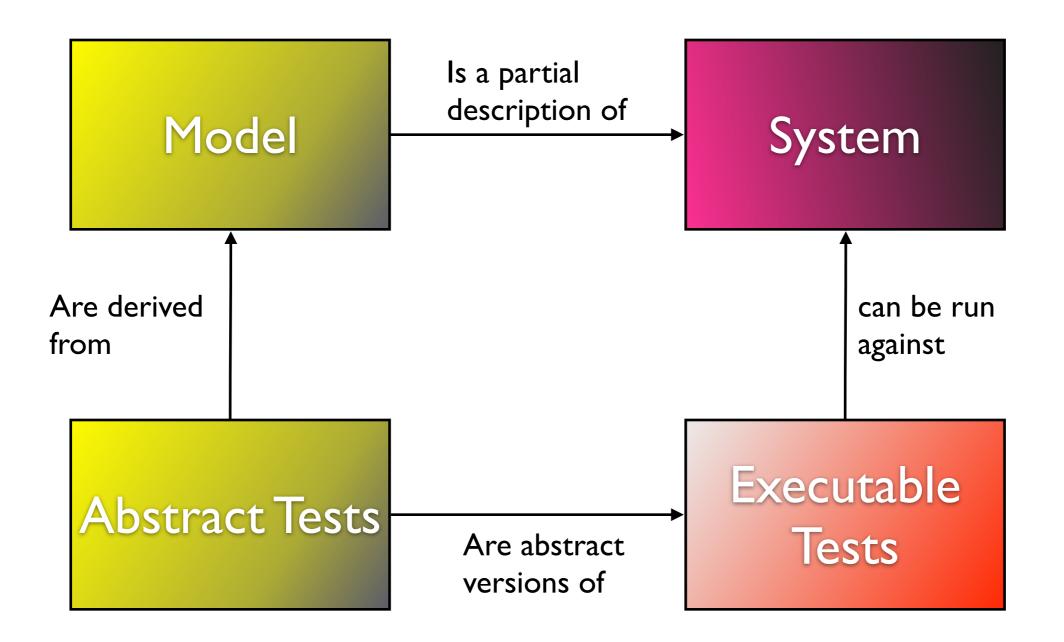
Validated Test Models

- The correctness and completeness of the test model is crucial for the success of a model-based testing strategy
- In a model-driven approach to development and V&V, there are two variants for arriving at trustworthy test models
 - 1. Let the V&V team create a redundant model as test model
 - 2. Let the V&V team validate the existing design model and use that for test generation

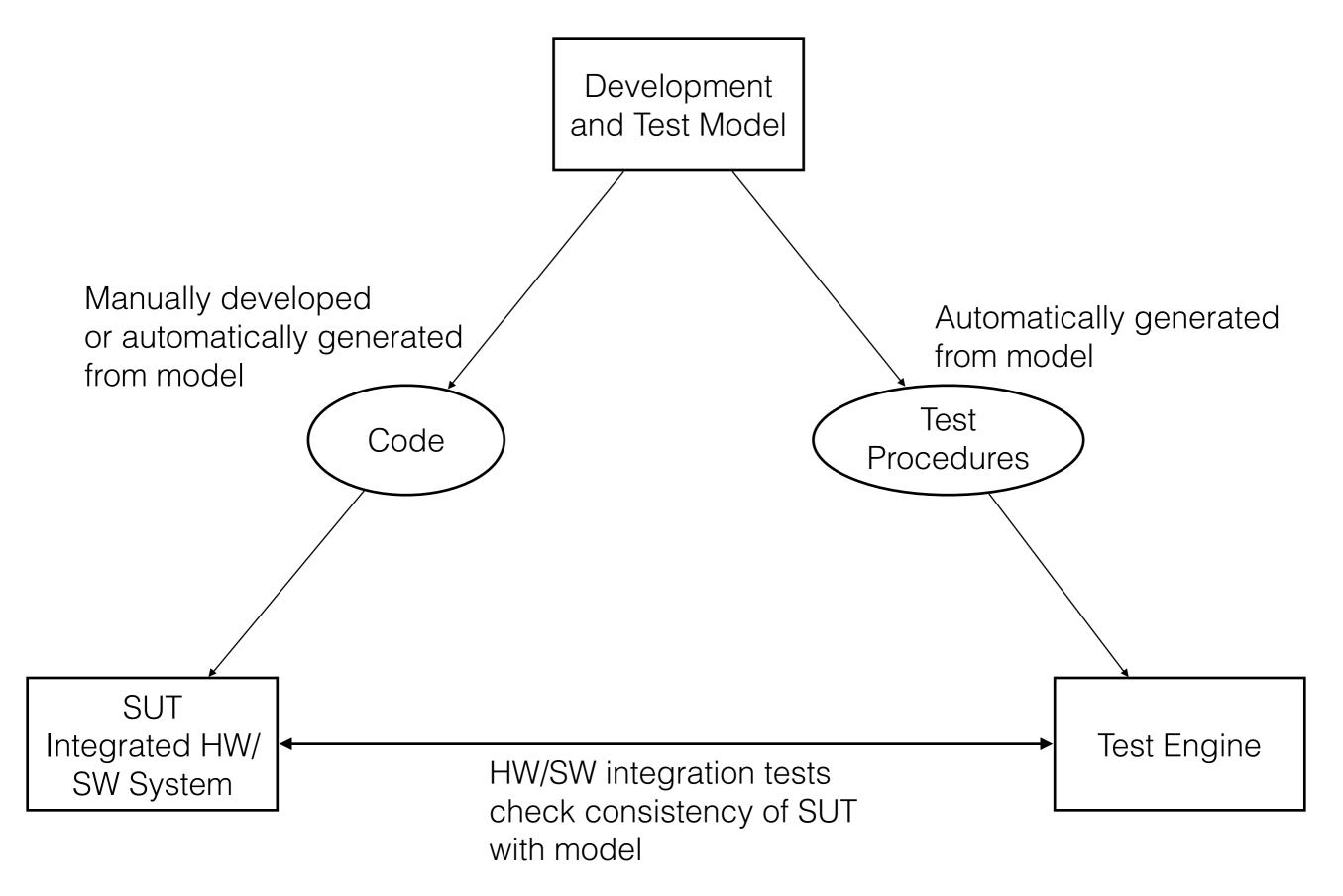
Validated Test Models – Variant 1



MBT-Paradigm



Validated Test Models – Variant 2



Validated Test Models

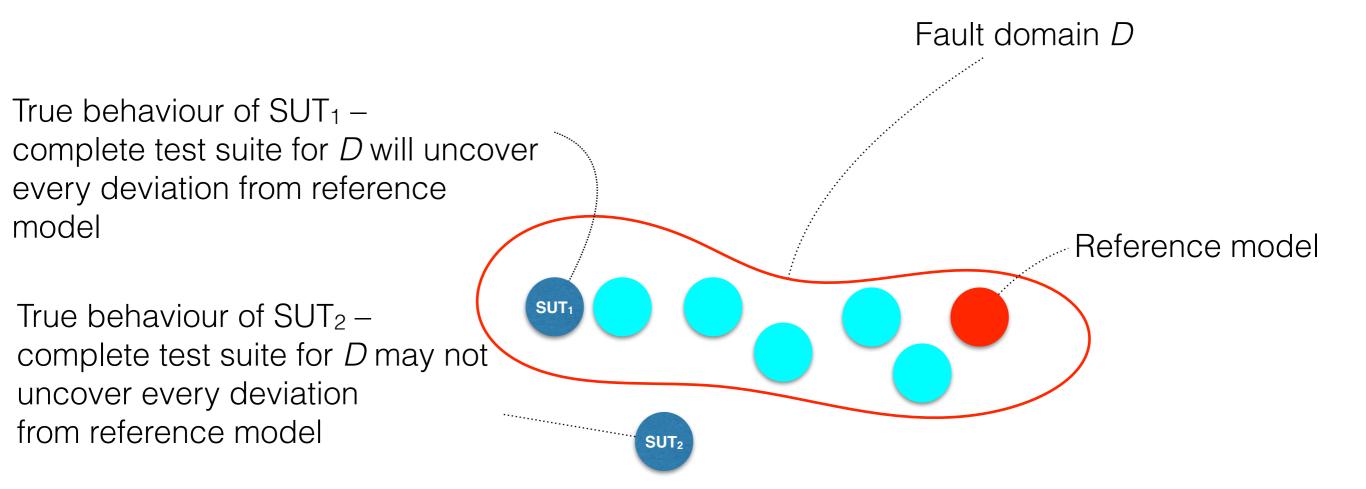
- We have seen in the RobustRailS presentations that complete verification of safety properties is possible for interlocking system designs of realistic size
 - This was achieved by bounded model checking in combination with inductive reasoning
 - Let's take this model and use it for test case generation . . .
 - . . . so we advocate Variant 2 described above

Complete Test Suites

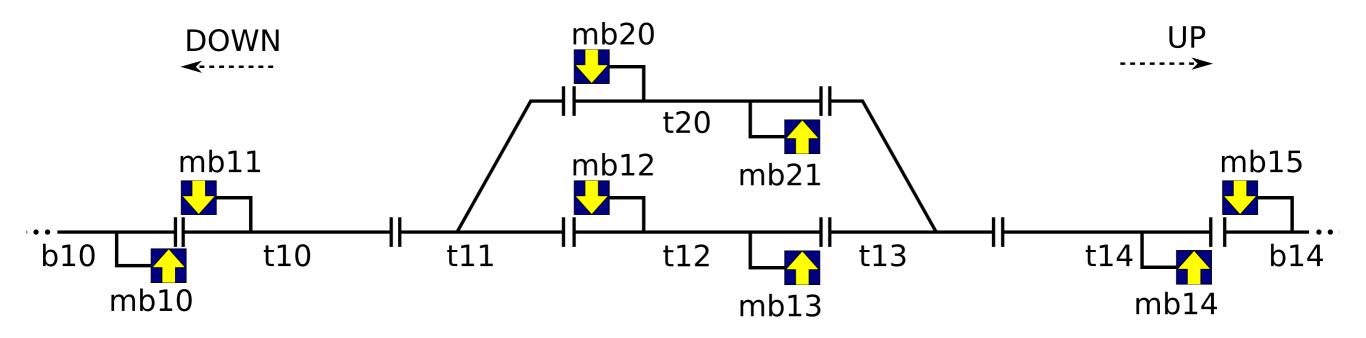
- For test suites created according to a certain strategy, we use the terms
 - Sound = correct implementations will not be rejected
 - Exhaustive = every faulty implementation will be detected
 - Complete = Exhaustive and Sound

Complete Test Suites

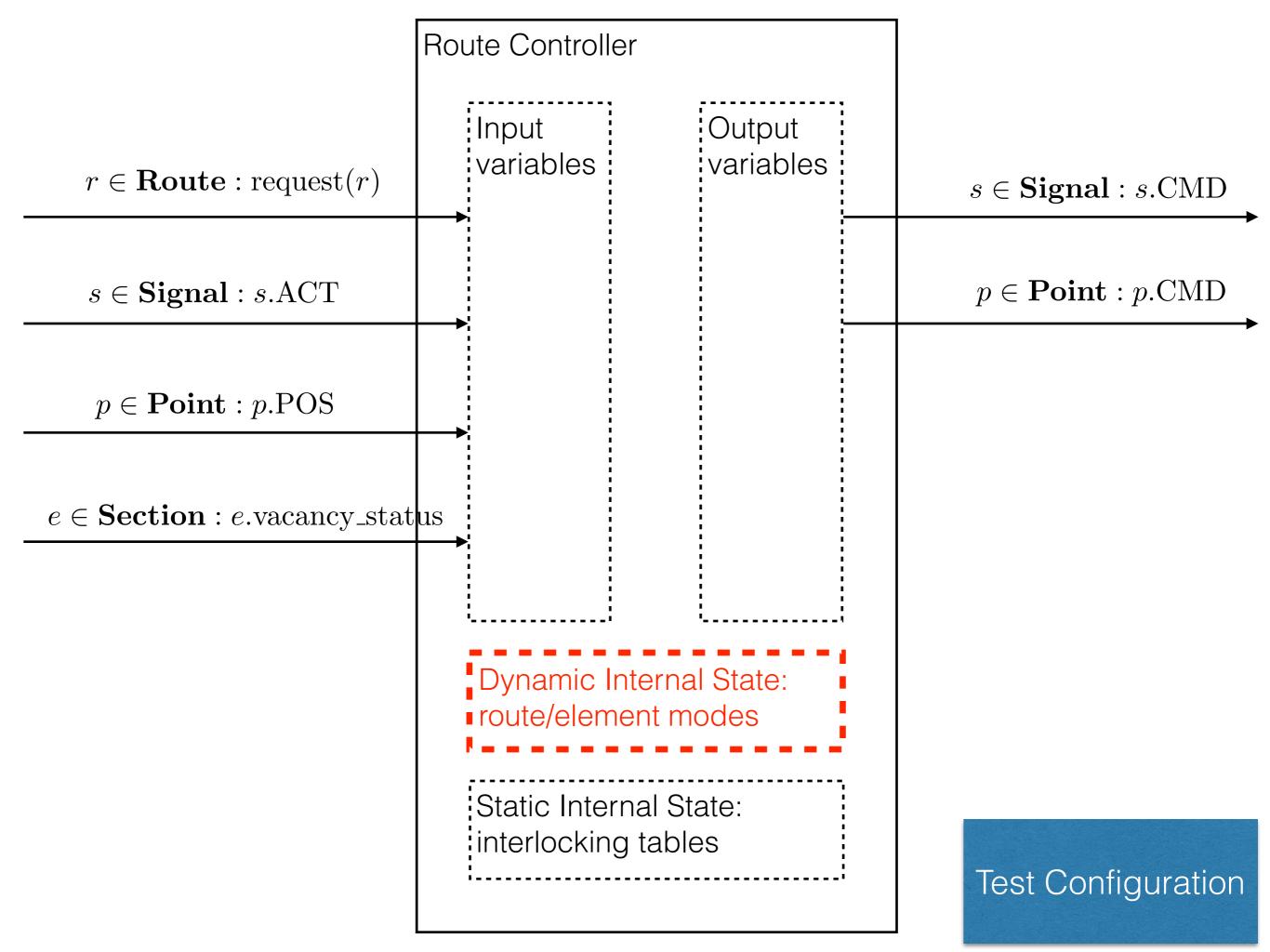
- For black-box testing, completeness depends on a prespecified fault domain
 - The true behaviour of the system under test must be captured in a (very large) class of models that may or may not be correct in relation to the given reference model



How many test cases do we need – naive approach



Example of railway network to be controlled

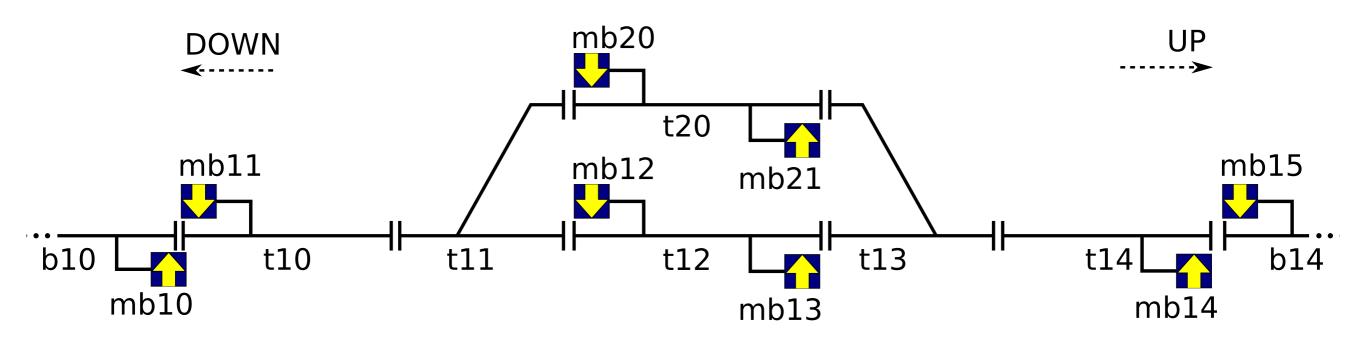


How many test cases – naive approach

• A complete test suite requires test cases in the order of magnitude of at least

$$|\Sigma_I| \cdot n^2$$
 test cases

 $\Sigma_I = \text{set of possible input vectors to the controller}$ $|\Sigma_I| = \text{Number of input vectors}$ n = number of internal states



Inputs.

b10, t0, t11,..., b14 \in {FREE, LOCKED, OCCUPIED}

plus

t11.pos, t13.pos $\in \{PLUS, MINUS\}$

plus

 $mb11, \dots, mb15 \in \{HALT, GO\}$

This results in

 $|\Sigma_I| = (8 \cdot 3) \cdot (2 \cdot 2) \cdot (8 \cdot 2) = 1536$

Internal State.

Number of states in route controller^{Number of routes}

This implies

 $n = (4 + \text{Number of segments in route})^8 \approx 6^8$

Therefore order of magnitude for the number of test cases needed for complete test suite is

$$|\Sigma_I| \cdot n^2 = 1536 \cdot 6^{16} \approx 4.3 \cdot 10^{15}$$

Suppose a system test case execution needs 60s. Then you need

$$\frac{4.3 \cdot 10^{15}}{(60 \cdot 24 \cdot 365)} \approx 8 \cdot 10^9 \quad \text{years to execute the test suite}$$

Internal State.

Number of states in route controller^{Number of routes}

This implies

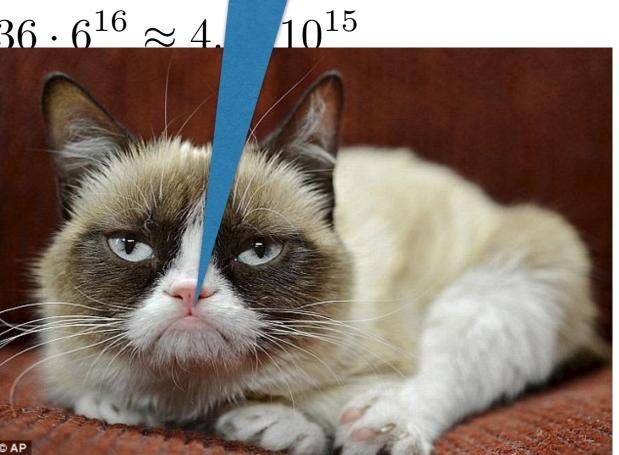
n = (4 + Number of se)

Therefore order of magnitude for the null of test cases needed for complete test suite is

$$|\Sigma_I| \cdot n^2 = 1536 \cdot 6^{16} \approx 4.$$

Suppose a system test case execution Then you need

$$\frac{4.3 \cdot 10^{15}}{(60 \cdot 24 \cdot 365)} \approx 8 \cdot 10^9$$



Boring!

A refined test strategy . . .

. . . in three steps

- Compositional Reasoning
- Equivalence Class Testing
- Randomisation in combination with boundary value selection

Compositional Reasoning

- From the knowledge about asserted behaviour of components . . .
- ... conclude about the behaviour of the integrated system

More formally:

 C_i sat Specification_i, $i = 1, \ldots, n$

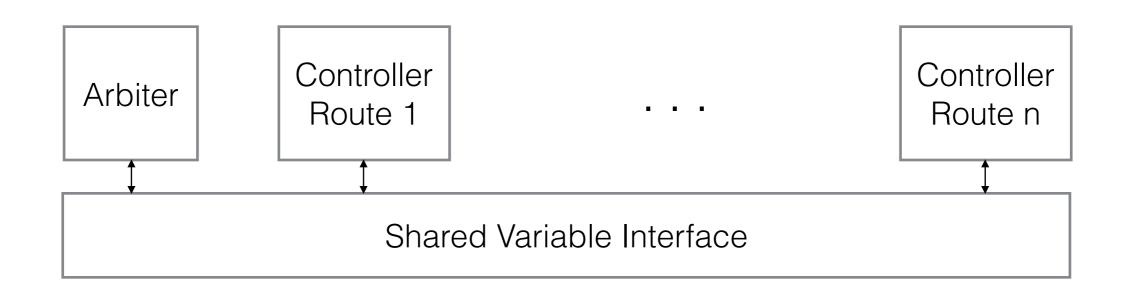
allows us to conclude that

$$(C_1 \parallel \cdots \parallel C_n)$$
 sat $\bigwedge_{i=1}^n$ Specification_i

provided that the integrated system is $\mathbf{compositional}$ – this is ensured, for example, if

- Components do not interfere with each other's internal state
- Data exchange over interfaces is synchronised

Application to Route Controller Tests



- synchronous execution
- synchronous data exchange over shared variables
- Arbiter acts a "semaphore" to ensure mutually exclusive route allocation

Refined Test Strategy

- Refinement A
 - Apply complete test suite on one route controller at a time
 - Conclude by compositional reasoning that whole system works correctly
 - This results in 8 x 1536 x 36 = 442368 test
 cases (Number of states)²
 Number of input vectors

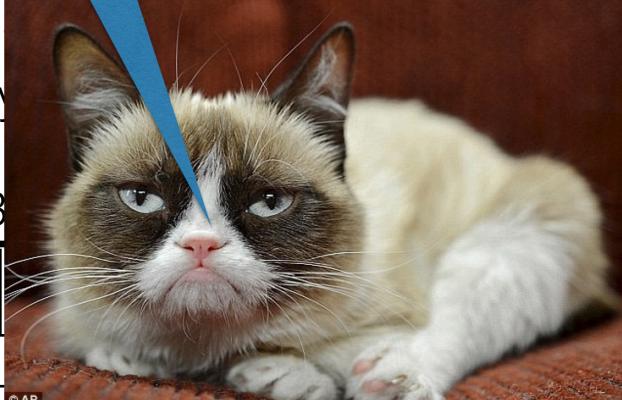
Number of routes

Still not satisfied! 442368 test cases for such a small network configuration! I'm not impressed!

Retric

 Apply complete test su at a time on one route controller

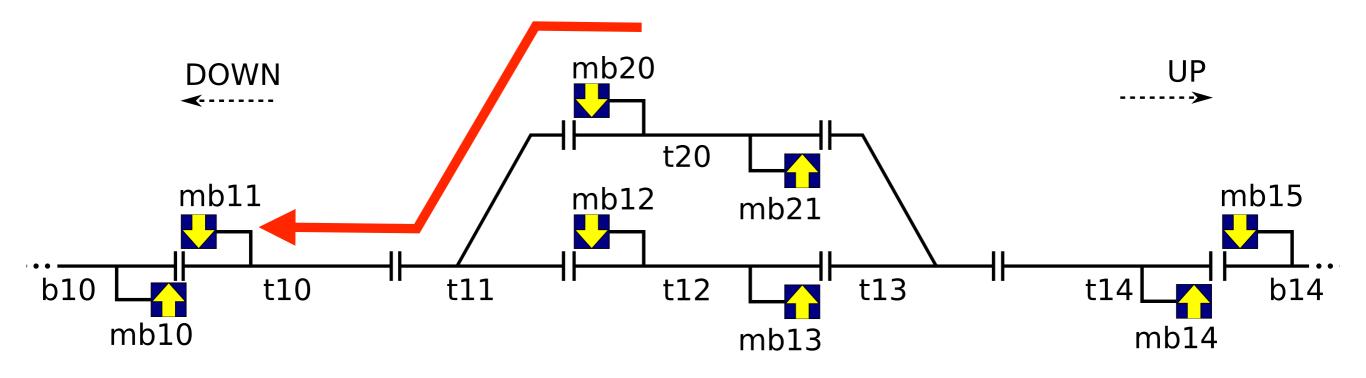
- Conclude by composi system works correctly
- This results in 8 x 153
 cases

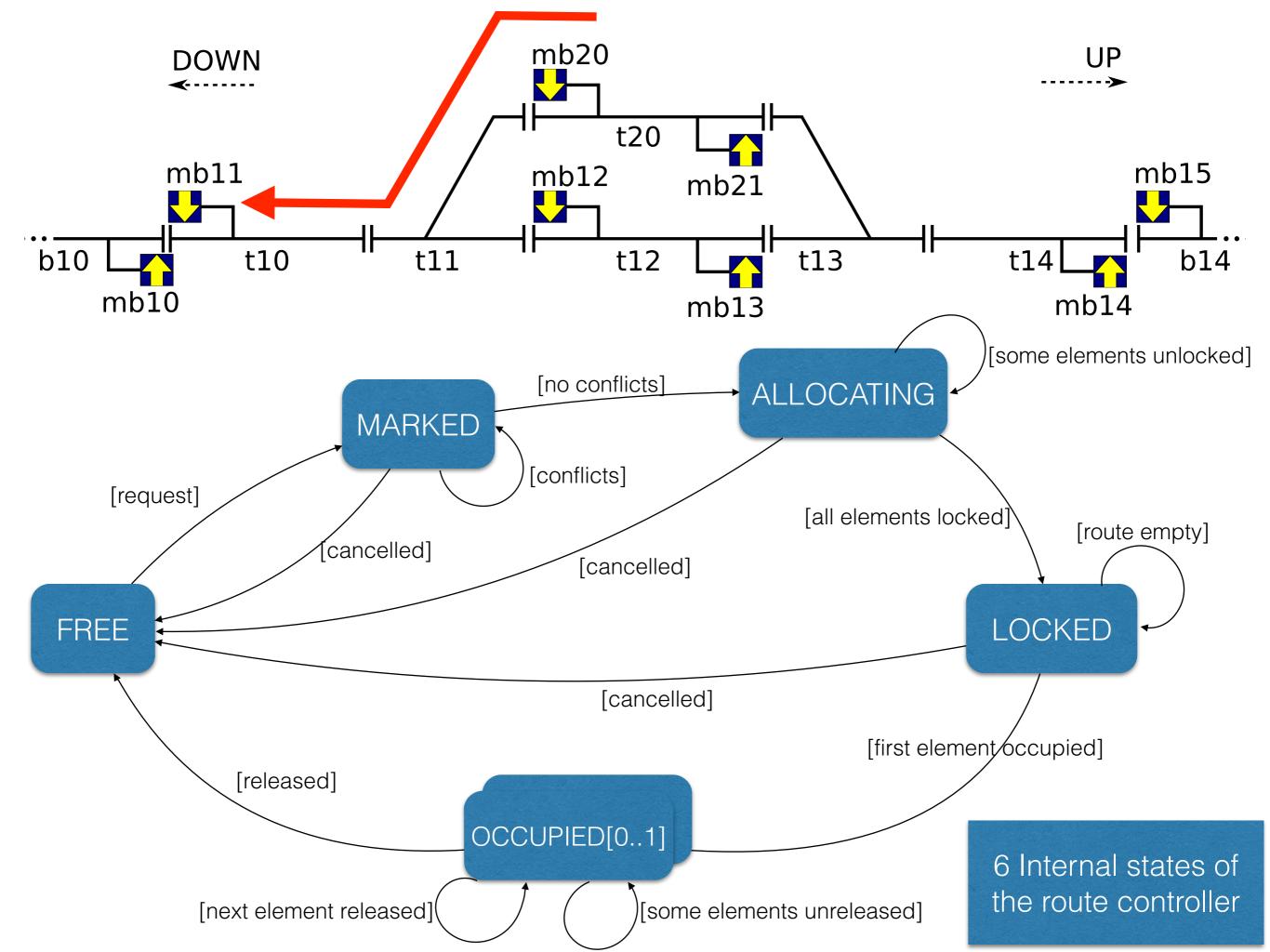


Refinement B – Input Equivalence Classes

- Recall
 - Input equivalence classes are constructed under the assumption that the SUT will process input of a class "in the same way"
- This intuitive concept can be formalised . . .
- . . . and also leads to a complete equivalence testing strategy

Example – Route(20,11)





Calculation of Input Equivalence Classes

conflicts $\equiv b10 \in \{L, O\} \lor t10 \in \{L, O\} \lor t11 \in \{L, O\} \lor$ route(10, 13) $\in \{A\} \lor$ route(10, 21) $\in \{A, L, O\} \lor$ route(12, 11) $\in \{A, L, O\}$

some elements unlocked \equiv t10 = F \lor t11 = F \lor t11.pos \neq minus \lor mb12 \neq HALT \lor mb10 \neq HALT

$$\begin{array}{ll} \mbox{Calculation of Input} \\ \mbox{Equivalence} \\ \mbox{internal state variables with arbitrary values} \\ \mbox{conflicts} \end{array} \equiv b10 \in \{L, O\} \lor t10 \in \{L, f\} \lor t11 \in \{L, O\} \lor \\ \mbox{route}(10, 13) \in \{A\} \lor \\ \mbox{route}(10, 21) \in \{A, L, O\} \lor \\ \mbox{route}(12, 11) \in \{A, L, O\} \end{array}$$

some elements unlocked \equiv $t10 = F \lor t11 = F \lor$ $t11.pos \neq minus \lor$ $mb12 \neq HALT \lor$ $mb10 \neq HALT$.

Calculation of Input Equivalence Classes

Every non-empty true/false combination of the six guard conditions defines one input equivalence class

6 guard conditions introduce approx. 64 input classes
 Number of test cases reduced to

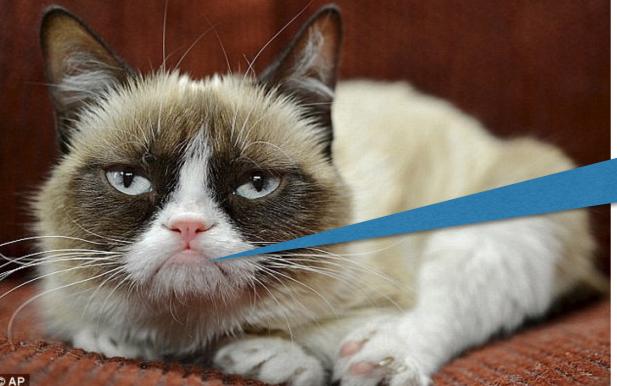
 $64x6^2 = 2304$ test cases per route

18432 test cases for all 8 routes

These can be automatically executed in 307h – or executed in parallel on 8 HW/SW integration test benches in 39h

Calculation of Input Equivalence Classes

^{ce} 6 guard conditions introduce approx. 64 input classes
 ^{ce} Number of test cases reduced to
 64x6² = 2304 test cases per route
 ^{ce} 18432 test cases for all 8 routes
 ^{ce} These can be automatically executed in 207h or
 ^{ce} HW/S^M Fair enough, but



Fair enough, but what about the assumption that SUT is inside the fault domain? Should we refine the input classes and the assumptions about internal SUT states?

- Refining the input classes and assuming more internal states in the SUT would widen the fault domain – the probability that the SUT is inside the domain would be increased
- But this refinement would lead to an exponential growth in the number of test cases

Refinement C – Combination With Random and Boundary Value Testing

- Instead of always using the same representative of each input class representative, select a random value of this class, whenever it is used in the test case – combine this technique with boundary value tests
- Completeness is still guaranteed for SUTs inside the fault domain
- For SUTs outside the fault domain, the test strength is significantly increased

Side Remark: Boundary Values of Logical Formulas

- These are the so-called MC/DC conditions of a formula
- A and B has MC/DC valuations (0,1), (1,0), (1,1)
- A or B has MC/DC valuations (0,0), (1,0), (0,1)
- Basic idea: check predicate valuations where exactly one atom is responsible for the formula to evaluate to true or false

Refinement C – Combination With Random and Boundary Value Testing

- Experimental results
 - Mutation score (= number of uncovered SUT failures) up to 99%, where naive random testing only achieves a score of 68%
 - Published in Felix Hübner, Wen-ling Huang, and Jan Peleska: Experimental Evaluation of a Novel Equivalence Class Partition Testing Strategy. In Blanchette and Kosmatov (eds.): Proceedings of the TAP 2015, Springer LNCS, Vol. 9154, pp. 155-173, 2015.

Conclusion

- Testing route controllers for interlocking systems can be improved with respect to
 - Compositional strategy from component tests to system integration tests
 - Application of a novel complete equivalence class testing strategy
 - Combination of this strategy with randomised value selection from input classes, including boundary values

Conclusion

- As a result,
 - Test cases are better justified (because they have been derived by complete strategy)
 - The resulting test suites have higher test strength then suites based on informal test selection criteria

Conclusion

Now why would anybody wish to ask a question about this stuff ?

y complete strategy)

have been derive

 The resulting test suite.
 then suites based on info criteria

